

AMENDMENTS TO THE SPECIFICATION

Please replace paragraph [0001], page 5 with the following:

However, even in the case of an LSI including a BIST circuit, when information of each abnormal bit is taken out to the outside and calculation is performed by an external computer as in a conventional technique described in Patent Document 1, a memory of the external computer has information on normality/abnormality of bits at all addresses. Thus a capacity of the memory is significantly consumed heavily, and also calculation takes much time.

Please replace paragraph [0002], page 17 with the following:

(2) The repair search circuit 30 determines whether either one of the addresses of the inputted X and Y address pair is set as an address to be masked (repaired) (hereinafter referred to as “mask-set”), that is, whether the mask bit 313X or 313Y is “1.” When the address is a mask-set address (hereinafter referred to as a “mask address”), the repair search circuit 30 discards the inputted X and Y address pair.